



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/072,658
Filing Date: February 7, 2002
Applicant: Furuhashi et al.
Group Art Unit: 2814
Examiner: Marcos D. Pizarro-Crespo
Title: Semiconductor Device And Method For Manufacturing
The Same
Attorney Docket: 9319S-000328

Mail Stop Non-Fee Amendment
Director of the United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE A

RECEIVED
SEP 26 2003
TECHNOLOGY CENTER 2000

10/072,658 Sir: 00072658 10072658
01 Furuhashi

In response to the Office Action mailed June 19, 2003, please amend the application as follows and consider the remarks set forth below.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 7 of this paper.

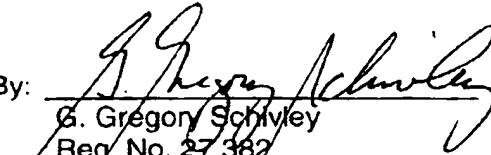
CONCLUSION

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant's representative therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action, and as such, the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested. If the Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at (248) 641-1600.

Respectfully submitted,

Dated: September 19, 2003

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. Box 828
Bloomfield Hills, Michigan 48303
(248) 641-1600
GGS/BEW/ps

By: 
G. Gregory Schivley
Reg. No. 27,382
Bryant E. Wade
Reg. No. 40,344